

ltem	Performance	Test Condition		
Electrical Performance	Test			
Inductance	Refer to standard electrical	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.		
DCR	characteristics list.	CH16502,Agilent33420A Micro-Ohm Mete		
Operating Temperature	-55℃~+125℃ (Including self - temperature	e rise)		
Storage temperature and Humidity range	110~+40℃,50~60%RH (Product with tap 255~+125℃ (on board)	ning)		
Saturation Current (Isat)	Approximately	Saturation DC Current (Isat) will cause L0 to drop \triangle L(%)		
Heat Rated Current (Irms)	Approximately	Heat Rated Current (Irms) will cause the coil temperature rise $\triangle T(^{\circ}C)$. 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer		
Reliability Test				
High Temperature Exposure(Storage) AEC-Q200	posure(Storage) iC-Q200 imperature Cycling iC-Q200 Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within±15% of initial value and shall not exceed the specification value. poisture Resistance ased Humidity	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature : 125±2°C Duration : 1000hrs Min. Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1 : -55±2°C 30min Min. Step2 : 125±2°C transition time 1min MAX. Step3 : 125±2°C 30min Min. Step4 : Low temp. transition time 1min MAX. Number of cycles : 1000 Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) 1.Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2.Raise temperature to 65±2°C 90-100%RH in 2.5hrs, an keep 3 hours, cool down to 25°C in 2.5hrs. 3.Raise temperature to 65±2°C 90-100%RH in 2.5hrs, an keep 3 hours, cool down to 25°C in 2.5hrs. 4.Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.		
Temperature Cycling AEC-Q200				
Moisture Resistance				
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) Humidity : $85\pm3\%$ R.H, Temperature : $85^{\circ}C\pm2^{\circ}C$ Duration : 1000hrs Min with 100% rated current. Measured at room temperature after placing for24±2hrs		



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Reliability Test							
High Temperature Operational Life (AEC-Q200)	 Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within±15% of initial value and shall not exceed the specification value. 	(IPC Tem Dura	onditioning: F C/JEDEC J-S perature : 12 ttion : 1000h sured at roon	TD-020D 25±2℃ irs Min. w	Classif	fication Re % rated c	eflow Profiles) urrent.
External Visual	Appearance:No damage	Inspect device construction, marking and workmanship. Electrical Test not required.					
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement			measurement		
Resistance to Solvents	Appearance:No damage.	Add	aqueous was	sh chemic	al - Ok	(EM clear	n or equivalent.
	Appearance : No damage. Impedance : within±15% of initial value Inductance : within±10% of initial value	Тур	e Peak value (g's)	Norm duration (Wave form	Velocity change (Vi)ft/sec
Mechanical Shock	Q : Shall not exceed the	SMI	D 100	6		Half-sine	12.3
	specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Lead 100 6 Half-sine 12.3 shocks in each direction along 3 perpendicular axes.					
Vibration		Osci Equi Tota Testi		ency: 10~ ation che .52mm±1	~2K~1 cker 0% 0 minut	10Hz for 2	0 minute
Resistance to Soldering Heat	eat Inductance : within±10% of initial value Q : Shall not exceed the 260±5 specification value. (solder temp) RDC : within ±15% of initial value and shall not exceed the and shall not exceed the preconditioning: (IPC/JEDEC J-S) Condition for 1 c Step1 : -55±2°C Step2 : 125±2°C Step3 : 125±2°C Number of cycles	mperature(° _C) 260±5	Time(s) 10±1	ramp and er	nperature /immersior mersion rat	te neat cycles	
Thermal shock (AEC-Q200)		Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1 : $-55\pm2^{\circ}$ C 15±1min Step2 : 125±2^{\circ} t5±1min Step3 : 125±2^{\circ} 15±1min Number of cycles : 300 Measured at room fempraturc after placing fo24±2hrs					



Item	Performance	Test Condition
Reliability Test		
ESD	Appearance:No damage.	lp 00% 10% tr tr Time (ns)
Solderability	More than 95% of the terminal electrode should be covered with solder ∘	Steam Aging: 16 hours \pm 15 min Preheat: 150°C,60sec. Solder: Sn96.5% Ag3% Cu0. 5% Temperature: 245 \pm 5°C \circ Flux for lead free: Rosin. 9.5% \circ Dip time: 4 \pm 1sec. Depth: completely cover the termination
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation
Flammability	Electrical Test not required	V-0 or V-1 are acceptable.
Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board.



Item	Performance	Test Condition
Reliability Test		
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.



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Reliability Test			
Soldering		NTEK terminations are suitable for all wave and cannot be avoided, the preferred technique is the	
Lead Free Solder re-flow:	Recommended temperature profiles for re-flow soldering in Figure 1.		
Soldering Iron (Figure 2):	limitations.	discouraged due to the inherent process control ployed the following precautions are recommended.	
О ЭЗИПСКАТО 217 200 150 60~180	EATING SOLDERING NATURAL COOLING TP(260°C / 10s max.) 0s.) 0s.) 0s.) 0s.) 0s.) 0s.) 0s.) 0s	FRE-HEATING SOLDERING NATURAL SOLDERING NATURAL SOLDERING VATURAL COOLING 150 150 150 150 150 150 150 150	
		Fig.2	